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Time Stamp	2005/05/12 10:42	2005/05/12 10:43	2005/05/12 10:50	2005/05/12 10:51	2005/05/12 10:52	2005/05/12 10:52	2005/05/12 10:52
Plurals	유 -		판	F.	판		PP.
Default Operator	80	OR	SO.	OR	N N	S.	OR
DBs	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
Search Query	((PLL) or (phase adj lock\$3 adj loop\$3)) and (phase adj (error or difference or offset) with (counter) and (lead with lag) with (counter)	((PLL) or (phase adj lock\$3 adj loop\$3)) and (phase adj (error or difference or offset)) with (first) adj (counter) and (lead with lag) with (second) adj (counter)	((PLL) or (phase adj loct\$3 adj locp\$3)) and (phase adj (error or difference or offset)) with (first) with (counter) and (lead with lag) with (second) with (counter)	((PLL) or (phase adj loct\$3 adj loop\$3) and phase adj detect\$3 with (multiple or plurality) with (counter\$3)	((PLL) or (phase adj lock\$3 adj loop\$3)) and phase adj detect\$3 with (multiple or plurality) with (counter\$3) and phase adj (difference or offset or error)	((PLL) or (phase adj lock\$3 adj loop\$3)) and phase adj detect\$3 with (multiple or plurality) with (counter\$3) and phase adj (difference or offset or error) and (lead with lag)	((PLL) or (phase adj lock\$3 adj locp\$3)) and phase adj detect\$3 with (multiple or plurality) with (counter\$3) and phase adj (difference or offset or error) and (up with down)
H SH	25	0	σ	22	23	13	27
# Ref	īS	23	ß	አ	S	8	72

Time Stamp	2005/05/02 17:48	2005/05/02 17:49	2005/05/02 17:50	2005/05/02 18:58	2005/05/02 18:58	2005/05/02 18:59	2005/05/02 18:59	2005/05/02 18:59	2005/05/02 19:00	2005/05/02 19:16	2005/05/02 19:16
Plurals	OFF	OFF	PFO	OFF	J-0	OFF	OFF	OFF.	- OFF	OFF	OFF
Default Operator	ĕ	ĕ	ĕ	ĕ	8	ĕ	g.	g S	g	g	R
DBs	USPAT	USPAT	USPAT; USOCR	US-PGPUB; USPAT; USOCR	USPAT	USPAT	USPAT	USPAT	USPAT	USPAT	USPAT
Search Query	(first and second) with (variable adj gain adj signal) with phase adj error	(first and second) with (variable adj gain adj signal) with phase adj (detect\$3 or comparat\$3)	("20030026372").PN.	("20030026372").PN.	first adj counter and second adj counter and phase adj (detect\$3 or comparat\$3)	((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter and second adj counter and phase adj (detect\$3 or comparat\$3)	((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter and second adj counter and phase adj (detect\$3 or comparat\$3) and phase adj error	((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter and second adj counter and phase adj (detect\$3 or comparat\$3) and phase adj error and up and down	((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter and second adj counter and phase adj (detect\$3 or comparat\$3) and phase adj (error or difference) with up with down	((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter with (up and down) and second adj counter with (up and down) and phase adj (detect\$3 or comparat\$3) and phase adj (error or difference) with up with down	((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter with (up and down) and second adj counter
Hits	0	0	0	-	682	436	124	18	25	E	32
# Ref	īS	23	8	5	SS	%	LS S3	8	65	S10	SII

2005/05/02 19:17	2005/05/02 19:17	2005/05/02 19:18	2005/05/02 19:18	2005/05/02 19:19	2005/05/02 19:19
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OR	8	8	Ř	ಕ	R
USPAT	USPAT	USPAT	USPAT	USPAT	USPAT
((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter with (up and down) and second adj counter with (up and down) and logic with lead with lag	((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter with (up and down) and second adj counter with (up and down) and pulse with lead with lag	((phase adj lock\$3 adj loop\$3) or (PLL)) and first adj counter with (up and down) and second adj counter with (up and down) and one with lead with lag	((phase adj lock\$3 adj loop\$3) or (PLL) and first adj counter with (up and down) and second adj counter with (up and down) and one with (first and second) adj signal	((phase adj lock\$3 adj loop\$3) or (PLL)) and counter with lead with lag	((phase adj lock\$3 adj loop\$3) or (PLL)) and (first with second) with counter with lead with lag
0	1	0	ĸ	97	6
S12	S13	S14	S15	S16	S17

Search History 5/12/2005 5:04:21 PM Page 2 C:\Documents and Settings\\mong\\my Documents\EAST\\montspaces\\09918809_4\\msp